

Data Sheet

Rev 02.1217

100Gbps QSFP28 BERT

iBERT X1



Description

The iBERT X1 100Gbps QSFP28 BERT is a 100Gbps Bit Error Rate Tester (BERT) with modularized interface which includes QSFP28 type connector. It complies with 100G MSA standards. The QSFP28 port follows QSFP28 MSA. User interface can individually monitor bit error rate, error count and timer via USB cable with PC. The serial ID and Digital Diagnostics Monitor for QSFP28 transceiver can also be monitored in the user interface.

Features

- 19" rackmount size
- Standard USB for connecting with PC.
- 100 Gbps QSFP28 interface module
- Friendly Graphic User Interface (GUI)

Applications

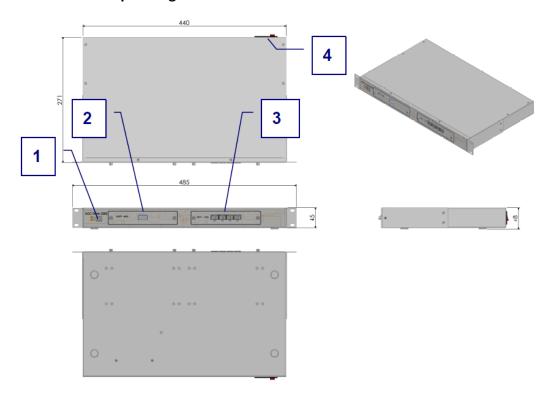
• 100 Gbps QSFP28 Transceiver Module Qualification

Specifications

Main Frame	
Slots	100Gbps QSFP28 for installing modules
Operating Temperature	0°C~50°C
Storage Temperature	-10°C ~ 70°C
Data Rate	100Gbps
Power Supply	100-240VAC
Weight	2.0kg
Dimensions (WxDxH)	485mm x 271mm x 48mm

Slot Boards	
QSFP28	2 standard QSFP28

Elements and Operating Instructions

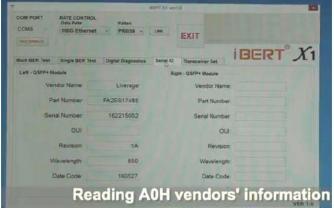


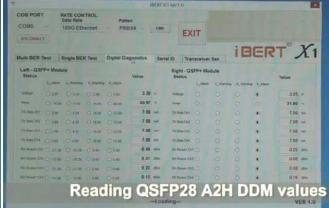
- 1. <u>USB (B Type)</u>: USB port. Connect to host via a standard USB cable
- 2. Slot L (Port 1): Slot for installing 100 Gbps QSFP28 interface module
- 3. Slot R (Port 2): Slot for installing 100 Gbps QSFP28 interface module
- 4. <u>Power Input</u>: AC power input

Graphical User Interface (GUI)









BERT Page:

- 1. <u>COM PORT</u>: The COM port number of the Tester. The number is defined in host hardware manager. This COM port number MUST be selected correctly.
- 2. RATE CONTROL: Transmitter/Receiver data rate standards selection.
- 3. LINK: Link with the tester. It shows PASS for correct setting of hardware connection and shows FAIL otherwise.
- 4. EXIT: Exit this program. It changes to START after click.
- 5. <u>Disconnect</u>: Disconnect and release the COM port link.
- 6. START: Start for the bit error rate test. It changes to STOP after click.
- 7. <u>STOP</u>: Stop for the bit error rate test.
- 8. RESET: Reset the bit error counts and rates.
- BERT Mode: Free run is for untimed testing; timer run is for limited time testing.

Digital Diagnostics Page:

This page shows the Digital Diagnostics data from transceivers. It complies with SFF-8472 for SFP+ and SFF-8436 for QSFP. Not support for SMA modules.

Serial ID Page:

This page shows the Serial ID from transceivers. It complies with SFF-8472 for SFP+ and SFF-8436 for QSFP. Not support for SMA modules.

Maintenance

Like any other type of electronic equipment, this QSFP checker should be kept away from water, high humidity, dust, electricity, and environments of extreme temperatures. Do not drop this tool on any hard surface. Internal modification of any of the SFP checker components can cause a malfunction and will invalidate the manufacturer's warranty.

Warranty

The manufacturer warrants this product to be free of defects in workmanship and materials for a period of 10 months after purchase. This warranty is solely limited to the repair or replacement of the original parts. All other costs are the sole responsibility of the owner. This warranty does not cover any defects, damage, or deterioration due to misuse, alteration, or negligence.

Ordering Information

Part Number	Description
S201314009999	iBERT X1 QSFP28 100G BERT Main Frame
S2013144	1-QSFP28 slot board

Note: Specifications are subject to change without notice.



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