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Data Sheet
Rev 01.1220

1.25Gbps TX:1550nm/RX:1310nm SMF 10km BiDi 2x5 SFF SC Optical Transceiver

CS5T3-24D-3S-xx-C

DESCRIPTION

The CS5T3-24D-3S-xx-C bi-directional 2x5 SFF (Small Form Factor) transceivers are designed for use in 1.25Gbps links up to 10km over a single strand single-mode fiber.

FEATURES

- Compliant with IEEE 802.3z Gigabit Ethernet
- Industry standard 2x5 footprint
- SC connector
- Single power supply 3.3V
- Differential LVPECL inputs and outputs
- Compatible with solder and aqueous wash processes
- Class 1 laser product compliant with EN 60825-1
- LD Type: DFB

APPLICATIONS

- Single-mode core fiber backbone links up to 10km
- GbE/1X Fiber Channel

PRODUCT OVERVIEW

PART NUMBER	INPUT/OUTPUT	SIGNAL DETECT	OPERATING TEMPERATURE
CS5T3-24D-3S-TC-C	AC/AC	LVTTL	0°C to 70°C
CS5T3-24D-3S-PC-C	DC/DC	LVPECL	0°C to 70°C
CS5T3-24D-3S-TI-C	AC/AC	LVTTL	-40°C to 85°C
CS5T3-24D-3S-PI-C	DC/DC	LVPECL	-40°C to 85°C

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTES
Storage Temperature	T_S	-40	85	°C	
Supply Voltage	V_{CC}	-0.5	4.0	V	
Input Voltage	V_{IN}	-0.5	V_{CC}	V	
Output Current	I_o	-	50	mA	
Operating Current	I_{OP}	-	400	mA	
Soldering Temperature	T_{SOLD}	-	260	°C	10 seconds on leads

RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTES
Case Operating Temperature	T_C	0	70	°C	CS5T3-24D-3S-xC-C
		-40	85		CS5T3-24D-3S-xI-C
Supply Voltage	V_{CC}	3.1	3.5	V	
Supply Current	$I_{TX} + I_{RX}$	-	300	mA	

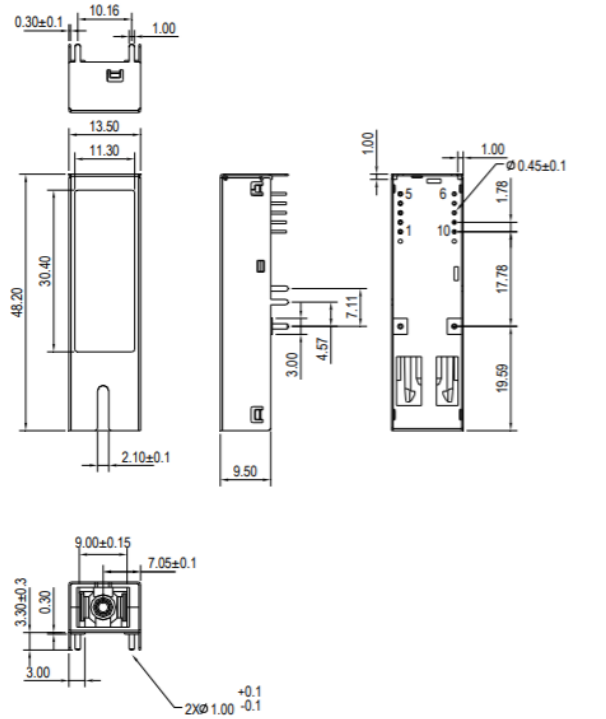
TRANSMITTER ELECTRO-OPTICAL CHARACTERISTICS (V_{CC} = 3.1V to 3.5V, T_C = 0°C to 70°C, -40°C to 85°C)

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNIT	NOTES
Data Rate	B	-	1250	1300	Mbps	
Output Optical Power 9/125um fiber	P_{out}	-9	-	-3	dBm	Average
Extinction Ratio	ER	9	-	-	dB	
Center Wavelength	λ_C	1530	1550	1570	nm	
Spectral Width (-20dB)	$\Delta\lambda$	-	-	1	nm	
Side Mode Suppression Ratio	SMSR	30	-	-	dB	
Rise/Fall Time, 10%~90%	$T_{r,f}$	-	-	260	ps	
Output Eye	Compliant with IEEE802.3z					
Output Power when Disabled	P_{OFF}			-45	dBm	Average
TX Disable Voltage-High		2			V	LVTTL
TX Disable Voltage-Low				0.8	V	LVTTL
Transmitter Data Input Voltage-High	$V_{IH} - V_{CC}$	-1.1	-	-0.74	V	DC Coupled
Transmitter Data Input Voltage-Low	$V_{IL} - V_{CC}$	-2.0	-	-1.58	V	DC Coupled
Transmitter Data Input Differential Voltage	V_{DIFF}	0.4	-	2.0	V	AC Coupled

RECEIVER ELECTRO-OPTICAL CHARACTERISTICS (V_{CC} = 3.1V to 3.5V, T_C = 0°C to 70°C, -40°C to 85°C)

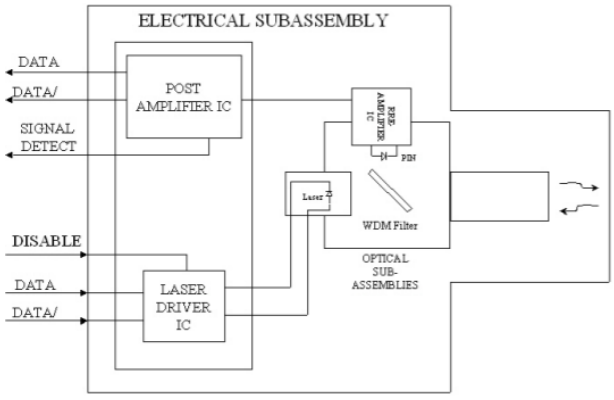
PARAMETER	SYMBOL	MIN	TYP.	MAX	UNIT	NOTES
Data Rate	B	-	1250	1300	Mbps	
Optical Input Power-Maximum	P_{IN}	-3	-	-	dBm	BER<10^{-12}
Optical Input Power-Minimum (Sensitivity)	P_{IN}	-	-	-21	dBm	BER<10^{-12}
Operating Center Wavelength	λ_C	1260	-	1360	nm	
Optical Isolation	ISO	-	-	-40	dB	λ=1480~1600nm
Return Loss	RL	-	-	-14	dB	λ=1260~1360nm
Signal Detect-Asserted	P_A	-	-	-21	dBm	Average
Signal Detect-Deasserted	P_D	-35	-	-	dBm	Average
Signal Detect-Hysteresis	P_A-P_D	1.0	-	-	dB	
Data Output Rise/Fall Time, 20%~80%	$T_{r,f}$	-	-	0.35	ns	
Signal Detect Output Voltage-High	V_{OH}-V_{CC}	-1.1	-	-0.74	V	LVPECL
Signal Detect Output Voltage-Low	V_{OL}-V_{CC}	-2.0	-	-1.58	V	LVPECL
Signal Detect Output Voltage-High	V_{OH}	2.4	-	V_{CC}	V	LVTTL
Signal Detect Output Voltage-Low	V_{OL}	0	-	0.4	V	LVTTL
Data Output Voltage-High	V_{OH}-V_{CC}	-1.1	-	-0.74	V	LVPECL
Data Output Voltage-Low	V_{OL}-V_{CC}	-2.0	-	-1.58	V	LVPECL
Data Differential Output Voltage	V_{DIFF}	0.6	-	1.8	V	AC Coupled

DRAWING DIMENSIONS



ALLDIMENSIONS ARE±0.20mm UNLESS OTHERWISE SPECIFIED
Unit : mm

BLOCK DIAGRAM OF TRANSCEIVER



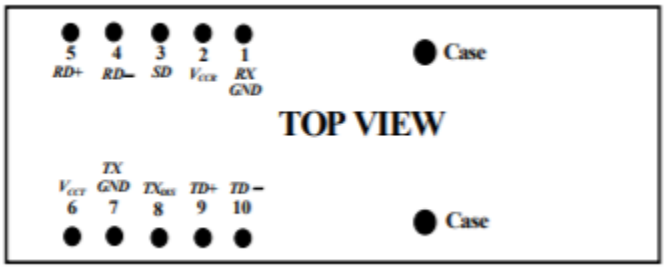
TOP VIEW

Transmitter and Receiver Optical Sub-Assembly Section - A 1550 nm InGaAsP laser and an InGaAs PIN photodiode integrate with an WDM filter to form a bi-directional single fiber optical subassembly (OSA). The laser of OSA is driven by a LD driver IC which converts differential input LVPECL logic signals into an analog laser driving current. The photodiode of OSA is connected to a circuit providing post-amplification quantization, and optical signal detection.

Transmitter Disable - Transmitter Disable is a LVTTL control pin. To disable the module, connect this pin to +3.3 V LVTTL logic high “1”. To enable module, connect to LVTTL logic low “0”.

Receiver Signal Detect - Signal Detect is a basic fiber failure indicator. This is a single-ended LVPECL/LVTTL output. As the input optical power is decreased, Signal Detect will switch from high to low (deassert point) somewhere between sensitivity and the no light input level. As the input optical power is increased from very low levels, Signal Detect will switch back from low to high (assert point). The assert level will be at least 1.0 dB higher than the deassert level.

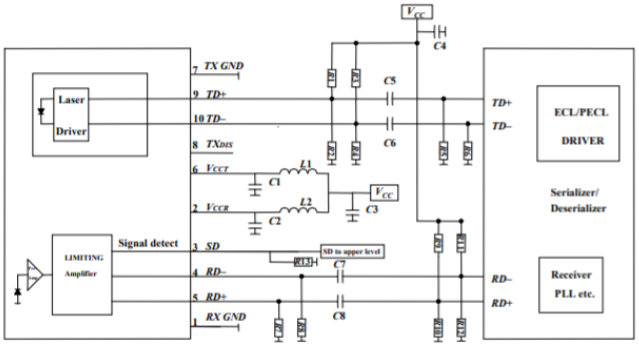
CONNECTION DIAGRAM



PIN	SYMBOL	DESCRIPTION
1	*RX GND*	Receiver Signal Ground, Directly connect this pin to the receiver ground plane.
2	V_{CCR}	Receiver Power Supply Provide +3.3 Vdc via the recommended receiver power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCR} pin.
3	*SD*	Signal Detect. Normal optical input levels to the receiver result in a logic "1" output, V_{OH}, asserted. Low input optical levels to the receiver result in a fault condition indicated by a logic "0" output V_{OL}, deasserted Signal Detect is a single-ended LVPECL output. *SD* can be terminated with LVPECL techniques via 50Ω to V_{CCR} – 2 V. Alternatively, *SD* can be loaded with a 180 Ω resistor to *RX GND* to conserve electrical power with small compromise to signal quality. If Signal Detect output is not used, leave it open-circuited.
4	*RD–*	*RD–* is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
5	*RD+*	*RD+* is an open-emitter output circuit. Terminate this high-speed differential LVPECL output with standard LVPECL techniques at the follow-on device input pin. (See recommended circuit schematic)
6	V_{CCT}	Transmitter Power Supply Provide +3.3 Vdc via the recommended transmitter power supply filter circuit. Locate the power supply filter circuit as close as possible to the V_{CCT} pin.
7	*TX GND*	Transmitter Signal Ground Directly connect this pin to the transmitter signal ground plane. Directly connect this pin to the transmitter ground plane.
8	TX_{DIS}	Transmitter Disable Connect this pin to +3.3V LVTTL logic high "1" to disable transmitter. To enable module connect to LVTTL logic low "0" or open.
9	*TD+*	Transmitter Data In Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)
10	*TD–*	Transmitter Data In-Bar Terminate this high-speed differential LVPECL input with standard LVPECL techniques at the transmitter input pin. (See recommended circuit schematic)

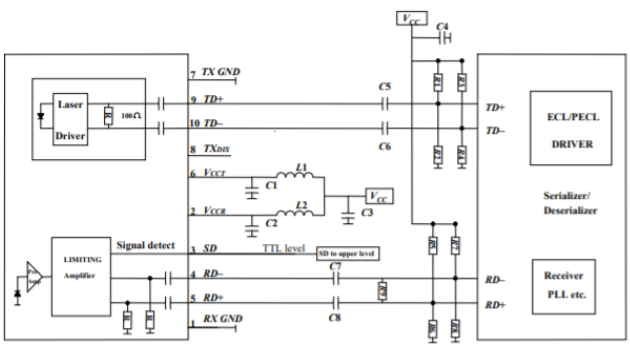
RECOMMENDED CIRCUIT SCHEMATIC

DC/DC Coupling



C1/C2/C4/C5/C6/C7/C8 = 100 nF *C3* = 4.7 *μF* L1/L2 = 1 *μH*
R1/R3 = 82Ω *R2/R4* = 130 Ω *R7/R8* = 130 Ω
R13 = 180 Ω (PECL) *R5/R6/R9/R10/R11/R12* Depend on SerDes

AC/AC Coupling



C1/C2/C4/C5/C6/C7/C8 = 100 nF *C3* = 4.7 *μF* *L1/L2* = 1*μH*
R1/R2/R3/R4/R5/R6/R7/R8/*R9* Depend on SerDes

In order to get proper functionality, a recommended circuit is provided in above recommended circuit schematic. When designing the circuit interface, there are a few fundamental guidelines to follow.

(1) The differential data lines should be treated as 50 Ω Micro strip or strip line transmission lines. This will help to minimize the parasitic inductance and capacitance effects. Locate termination at the received signal end of the transmission line. The length of these lines should be kept short and of equal length.

(2) For the high speed signal lines, differential signals should be used, not single-ended signals, and these differential signals need to be loaded symmetrically to prevent unbalanced currents which will cause distortion in the signal.

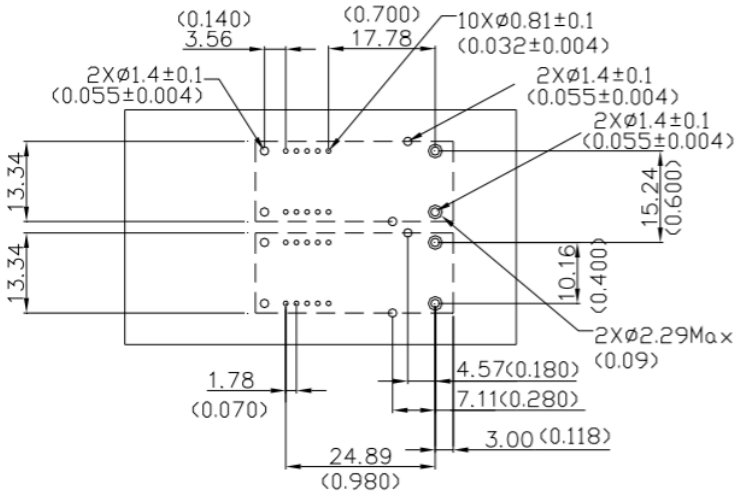
(3) Multi layer plane PCB is best for distribution of VCC, returning ground currents, forming transmission lines and shielding, Also, it is important to suppress noise from influencing the fiber-optic transceiver performance, especially the receiver circuit.

(4) A separate proper power supply filter circuits shown in Figure for the transmitter and receiver sections. These filter circuits suppress VCC noise over a broad frequency range, this prevents receiver sensitivity degradation due to VCC noise.

(5) Surface-mount components are recommended. Use ceramic bypass capacitors for the 0.1 µF capacitors and a surface-mount coil inductor for 1 µH inductor. Ferrite beads can be used to replace the coil inductors when using quieter VCC supplies, but a coil inductor is recommended over a ferrite bead. All power supply components need to be placed physically next to the VCC pins of the receiver and transmitter.

(6) Use a good, uniform ground plane with a minimum number of holes to provide a low-inductance ground current return for the power supply currents.

RECOMMENDED BOARD LAYOUT HOLE PATTERN



Unit : mm(inches)

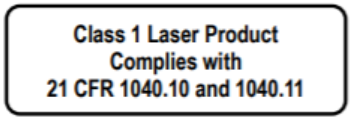
This transceiver is compatible with industry standard wave or hand solder processes. After wash process, all moisture must be completely removed from the module. The transceiver is supplied with a process plug to prevent contamination during wave solder and aqueous rinse as well as during handling, shipping or storage.

Solder fluxes should be water-soluble, organic solder fluxes. Recommended cleaning and degreasing chemicals for these transceivers are alcohol's (methyl, isopropyl, isobutyl), aliphatics (hexane, heptane) and other chemicals, such as soap solution or naphtha. Do not use partially halogenated hydrocarbons for cleaning/degreasing.

EYE SAFETY MARK

The single-mode transceiver is a class 1 laser product. It complies with EN 60825-1 and FDA 21 CFR 1040.10 and 1040.11. In order to meet laser safety requirements, the transceiver shall be operated within the Absolute Maximum Ratings.

Required Mark



[Caution] All adjustments have been done at the factory before the shipment of the devices. No maintenance and user serviceable part is required. Tampering with and modifying the performance of the device will result in voided product warranty.

Additional Notes

- Avoid eye or skin exposure to laser radiations.
- The device is sensitive to electro-static discharge (ESD). The device should be handled with ESD proof tools. To assemble the device on PCB, proper grounding is required to prevent ESD.
- Specifications are subject to change without notice.

Lasermate Group, Inc.
19608 Camino De Rosa
Walnut, CA 91789 USA
Tel: (909)718-0999
Fax: (909)718-0998
sales@lasermate.com
www.lasermate.com