



# 1.25Gbps TX:1550nm/RX:1310nm MMF 550m BiDi SFP LC Optical Transceiver

## CM5T3-24H-3S-Tx-L



### DESCRIPTION

The CM5T3-24H-3S-Tx-L bi-directional SFP (Small Form Pluggable) transceivers are designed for use in 1.25Gbps links up to 550m over a single strand multi-mode fiber.

### FEATURES

- Industry standard small form pluggable (SFP) package
- Simplex LC connector
- Single power supply 3.3V
- Differential LVPECL inputs and outputs
- TTL signal detect indicator
- Hot pluggable
- Class 1 laser product compliant with EN 60825-1
- Input/Output: AC/AC
- Signal Detect: TTL
- LD Type: 1550 FP

### APPLICATIONS

- Multi-mode core fiber backbone links up to 550m
- 1000Base Bidi

### PRODUCT OVERVIEW

PART NUMBER	OPERATING TEMPERATURE
CM5T3-24H-3S-TC-L	0°C to 70°C
CM5T3-24H-3S-TI-L	-40°C to 85°C

## DIAGNOSTICS

PARAMETER	RANGE	ACCURACY	UNIT	CALIBRATION
Temperature	-40 to 95	±3	°C	External
Voltage	3.0 to 3.6	±0.1	V	
Bias Current	0 to 90	±10%	mA	
TX Power	-10 to +2	±3 dB	dBm	
RX Power	-18 to 0	±3 dB	dBm	

## ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTES
Storage Temperature	T <sub>S</sub>	-40	85	°C	
Supply Voltage	V <sub>CC</sub>	-0.5	4.0	V	
Input Voltage	V <sub>IN</sub>	-0.5	V <sub>CC</sub>	V	

## RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTES
Case Operating Temperature	T <sub>C</sub>	0	70	°C	CM5T3-24H-3S-TC-L
		-40	85		CM5T3-24H-3S-TI-L
Supply Voltage	V <sub>CC</sub>	3.1	3.5	V	
Supply Current	I <sub>TX</sub> + I <sub>RX</sub>		250	mA	

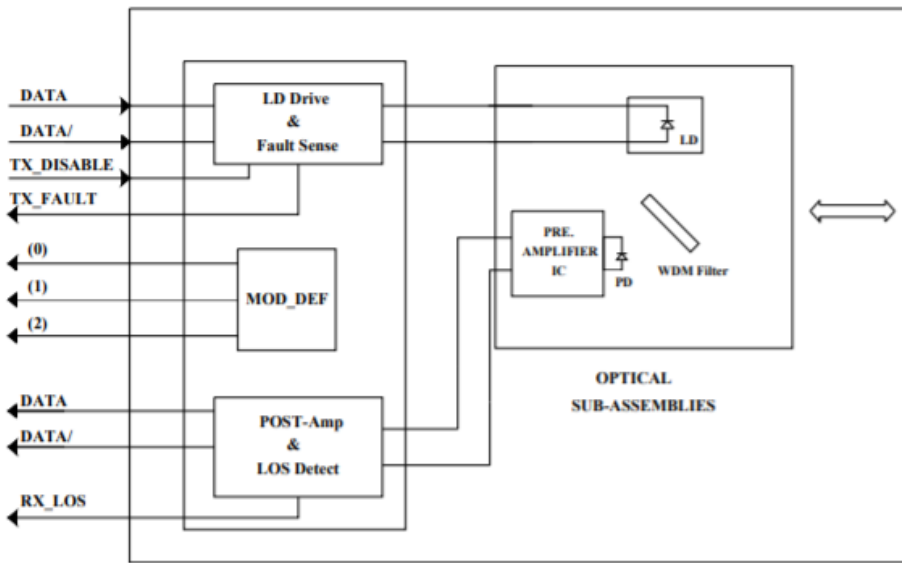
TRANSMITTER ELECTRO-OPTICAL CHARACTERISTICS (V<sub>CC</sub> = 3.1V to 3.5V, T<sub>C</sub> = 0°C to 70°C, -40°C to 85°C)

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNIT	NOTES
Output Optical Power 62.5/125um fiber	P <sub>OUT</sub>	-8	-	0	dBm	Average
Extinction Ratio	ER	9	-	-	dB	
Center Wavelength	λ <sub>C</sub>	1480	1530	1580	nm	
Spectral Width (RMS)	Δλ	-	-	4.0	nm	
Rise/Fall Time (20%~80%)	T <sub>r, f</sub>	-	-	260	ps	
Total Jitter	TJ	-	-	227	ps	
Output Eye	Compliant with IEEE802.3z					
Max. P <sub>OUT</sub> TX-DISABLE Asserted	P <sub>OFF</sub>	-	-	-45	dBm	
Differential Input Voltage	V <sub>DIFF</sub>	0.4	-	2.0	V	

RECEIVER ELECTRO-OPTICAL CHARACTERISTICS (V<sub>CC</sub> = 3.1V to 3.5V, T<sub>C</sub> = 0°C to 70°C, -40°C to 85°C)

PARAMETER	SYMBOL	MIN	TYP.	MAX	UNIT	NOTES
Optical Input Power-Maximum	P <sub>IN</sub>	0	-	-	dBm	BER<10 <sup>-12</sup>
Optical Input Power-Minimum (Sensitivity)	P <sub>IN</sub>	-	-	-18	dBm	BER<10 <sup>-12</sup>
Operating Center Wavelength	λ <sub>C</sub>	1260	-	1360	nm	
Optical Return Loss	ORL	14	-	-	dB	λ=1260~1360nm
Optical Isolation	ISO	-	-	-40	dB	λ=1480~1580nm
Loss of Signal-Asserted	P <sub>A</sub>	-	-	-18	dBm	
Loss of Signal-Deasserted	P <sub>D</sub>	-35	-	-	dBm	
Differential Output Voltage	V <sub>DIFF</sub>	0.5	-	1.2	V	
Data Output Rise, Fall Time (20%~80%)	T <sub>r, f</sub>	-	-	0.35	ns	
Receiver Loss of Signal Output Voltage-Low	RX_LOS <sub>L</sub>	0	-	0.5	V	
Receiver Loss of Signal Output Voltage-High	RX_LOS <sub>H</sub>	2.4	-	V <sub>CC</sub>	V	

**BLOCK DIAGRAM OF TRANSCEIVER**

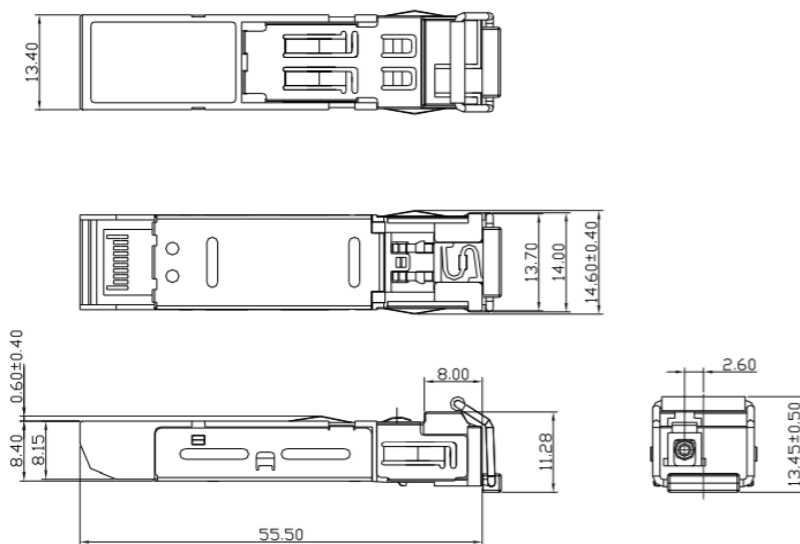


**Transmitter and Receiver Optical Sub-Assembly Section** - A 1550 nm InGaAsP laser and an InGaAs PIN photodiode integrate with an WDM filter to form a bi-directional single fiber optical subassembly (OSA). The laser of OSA is driven by a LD driver IC which converts differential input LVPECL logic signals into an analog laser driving current. The photodiode of OSA is connected to a circuit providing post-amplification quantization, and optical signal detection.

**TX\_DISABLE** - The TX\_DISABLE signal is high (TTL logic “1”) to turn off the laser output.

**Receive Loss (RX\_LOS)** - The RX\_LOS is high (logic “1”) when there is no incoming light from the companion transceiver. This signal is normally used by the system for the diagnostic purpose. The signal is operated in TTL level.

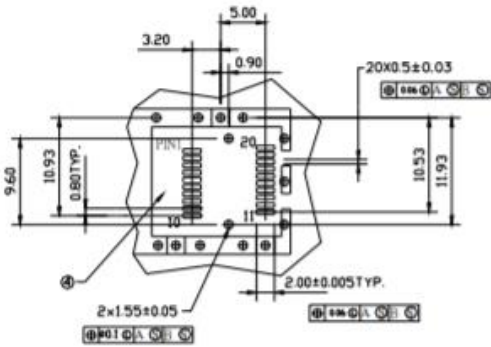
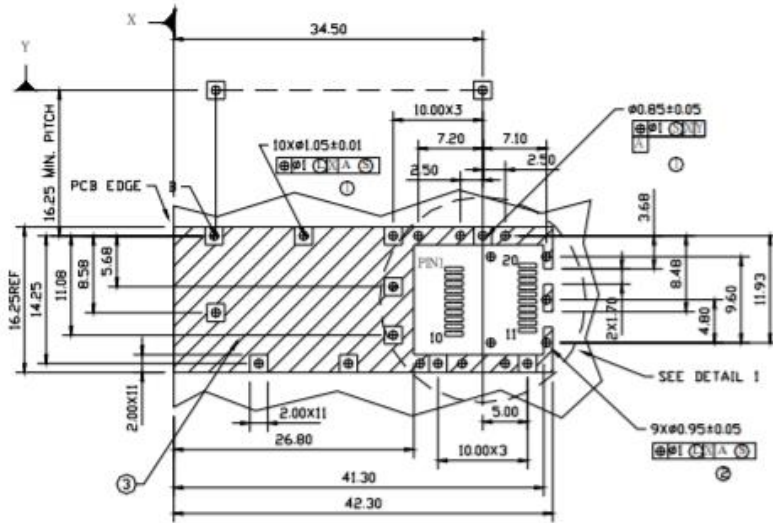
**DIMENSIONS**



**DIMENSIONS ARE IN MILLIMETERS**

**ALL DIMENSIONS ARE ± 0.2mm UNLESS OTHERWISE SPECIFIED**

SFP HOST BOARD MECHANICAL LAYOUT



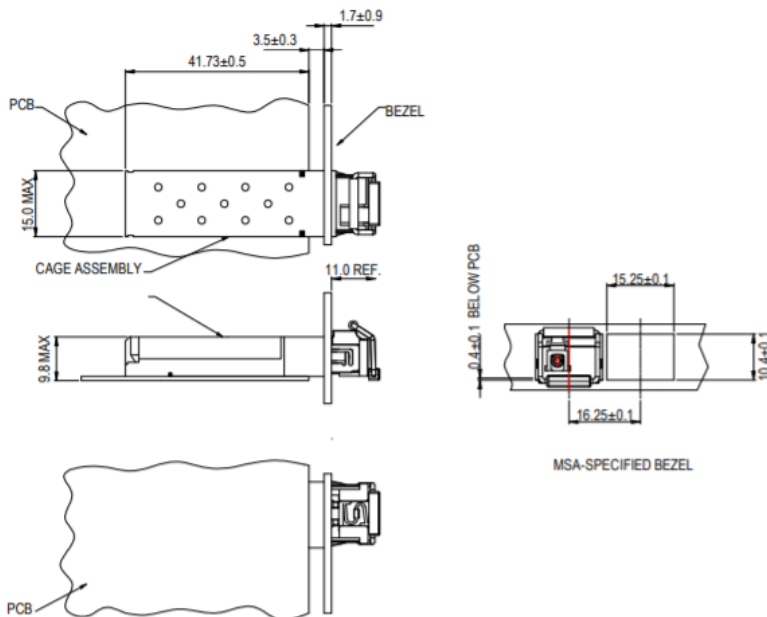
DETAIL 1

LEGEND

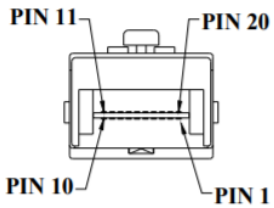
1. PADS AND VIAS ARE CHASSIS GROUND
2. THROUGH HOLES, PLATING OPTIONAL
3. HATCHED AREA DENOTES COMPONENT AND TRACE KEEPOUT (EXCEPT CHASSIS GROUND)
4. AREA DENOTES COMPONENT KEEPOUT (TRACES ALLOWED)

DIMENSIONS ARE IN MILLIMETERS

ASSEMBLY DRAWING (unit: mm)



**PIN ASSIGNMENT**



Pin	Signal Name	Description
1	$T_{GND}$	Transmit Ground
2	$TX\_FAULT$	Transmit Fault
3	$TX\_DISABLE$	Transmit Disable
4	$MOD\_DEF (2)$	SDA Serial Data Signal
5	$MOD\_DEF (1)$	SCL Serial Clock Signal
6	$MOD\_DEF (0)$	TTL Low
7	$RATE\_SELECT$	Open Circuit
8	$RX\_LOS$	Receiver Loss of Signal, TTL High, open collector
9	$R_{GND}$	Receiver Ground
10	$R_{GND}$	Receiver Ground
11	$R_{GND}$	Receiver Ground
12	$RX-$	Receive Data Bar, Differential PECL, ac coupled
13	$RX+$	Receive Data, Differential PECL, ac coupled
14	$R_{GND}$	Receiver Ground
15	$V_{CCR}$	Receiver Power Supply
16	$V_{CCT}$	Transmitter Power Supply
17	$T_{GND}$	Transmitter Ground
18	$TX+$	Transmit Data, Differential PECL, ac coupled
19	$TX-$	Transmit Data Bar, Differential PECL, ac coupled
20	$T_{GND}$	Transmitter Ground

**ADDITIONAL NOTES**

- Avoid eye or skin exposure to laser radiations.
- The device is sensitive to electro-static discharge (ESD). The device should be handled with ESD proof tools. To assemble the device on PCB, proper grounding is required to prevent ESD.
- Specifications are subject to change without notice.



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